

The AMALIA difference

AMALIA Platform is an advanced, comprehensive IP reuse solution.

Leveraging automation and AI/ML capabilities, it drastically reduces the complexity, cost and time traditionally associated with migrating and enhancing existing design IPs.

Four functional solutions provide a powerful portfolio to successfully accelerate analog reuse.

Save time with intelligent design migration



Navigating the design migration challenge

Migrating an IC or IP to a different process technology or node unlocks new revenue opportunities, enhancing performance and secure a reliable second source supply.

However, design migration is a conundrum for semiconductor business leaders. While it opens new business opportunities, analog and mixed-signal migrations are expensive and fraught with risks.

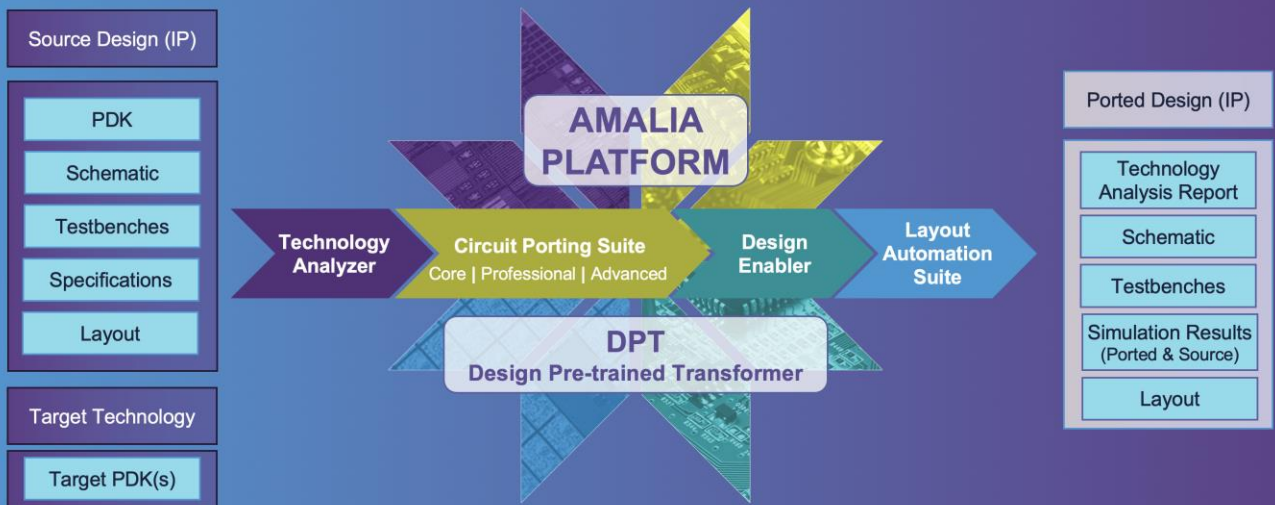
The AMALIA Platform addresses this challenge by providing a proven, unique end-to-end solution which reduces the engineering effort from man-months to man-weeks, reducing costs and significantly mitigating risks.

A unique integrated complete solution

Unlock the future of IC design with AMALIA's *designer-in-the-loop* approach. Automate the migration of analog, mixed-signal, and RF IC designs and IP to new process technologies, reducing design migration time from months to weeks while keeping engineers in control of sensitive design decisions.

Technology Analyzer - Unlock comprehensive insights into your base design and target technologies. Accelerate confirmation of your target process node and generate a device mapping file for a seamless migration.

Circuit Porting Suite - Effortlessly migrate design schematics and testbenches and exercise all the PVT corner simulations. It injects estimated parasitics into the target design to deliver better prediction of performance leading to earlier identification of potential issues.



Design Enabler - Accelerate Design-Centering and optimize your engineers' expertise with AMALIA AI-driven algorithms. Identify *Key Devices* impacting circuit performance and guide designers to make precise adjustments.

Layout Automation Suite - Preserve the intrinsic value demonstrated by production silicon, delivering the final step in the design migration flow. Leverage the AMALIA Design Pre-trained Transformer engine to perform analog physical implementation while leveraging the source layout and PDK. The migrated layout retains the existing floorplan ensuring DRC/LVS compliance and minimizes design iterations.

The AMALIA Platform seamlessly integrates with industry-leading design solutions from both Cadence® and Siemens EDA.

Experience the power of AMALIA contact us at sales@thalia-da.com