

AMALIA PLATFORM: CIRCUIT PORTING ADVANCED

Intelligent parasitic estimation in circuit porting

Building on the capabilities of *CP-Core* and *CP-Pro*, **Circuit Porting Advanced (CP-Adv)** offers a unique feature that intelligently incorporates *estimated parasitics* from the source layout into the ported design. The innovation significantly reduces the need for re-simulation and PEX iterations by 30-40%.

By streamlining the porting process, CP-Adv saves considerable time and helps identify potential design porting issues much earlier in the cycle.

Save more time: Reduced PEX and simulation iterations

Designer productivity

Reduces the number of iterations related to PEX by incorporating parasitics early in the design process.

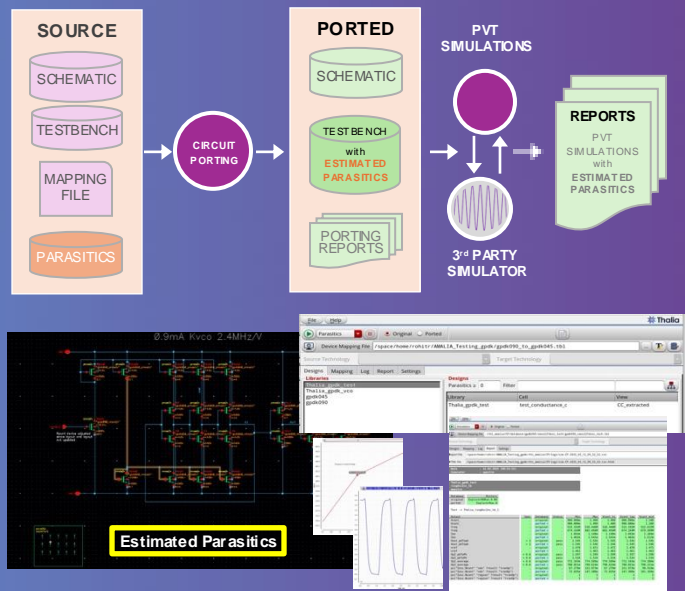
Porting cycle time reduce by 30-40%

Fully automated

CP-Pro can execute simulations in parallel dramatically shortening end-to-end simulation time (limited by compute and simulator license availability)

Identify performance issues sooner

Incorporating *estimated parasitics* delivers improved prediction of performance during porting, leading to earlier identification of potential issues



Key Features

Incorporates “Estimated Parasitics”

Based on source layout’s extracted parasitic view or from independent extraction run

Interactive comparison of parasitic connections

Parallelize verifications

Automate concurrent simulations and Maestro states limited only by compute and simulation resources

Comparison of PVT simulations (with parasitics)

Color coded reports comparing performance metrics versus the source database

Outputs

Ported schematic, testbench with parasitics
Simulation comparison, porting report & log file

Integrated with leading EDA solutions

Cadence® Virtuoso® ADE
Siemens EDA Calibre® xACT™ software, Analog FastSPICE

Trademark acknowledgment

Cadence and Virtuoso are Marks of Cadence Design Systems
Calibre and xACT are Marks of Siemens Industry Software Inc
Use of these names and other company’s and products does not imply endorsement



The AMALIA difference

AMALIA Platform is an advanced, comprehensive IP reuse solution.

Leveraging automation and AI/ML capabilities, it drastically reduces the complexity, cost and time traditionally associated with migrating and enhancing existing analog, mixed-signal and RF design IPs.

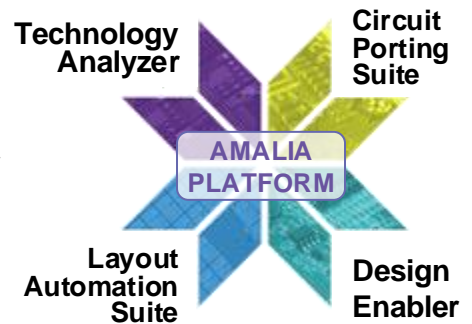
Four functional solutions provide a powerful portfolio to successfully accelerate analog reuse.

Navigating the design migration challenge

Migrating an IC or IP to a different process technology or node unlocks new revenue opportunities, enhancing performance and secure a reliable second source supply.

However, design migration is a conundrum for semiconductor business leaders. While it opens new business opportunities, analog and mixed-signal migrations are expensive and fraught with risks.

AMALIA Platform addresses this challenge by providing a proven, unique end-to-end solution which reduces the engineering effort from man-months to man-weeks, reducing costs while significantly mitigating risks.



Contact us today

Contact our sales team today to discuss how AMALIA can accelerate your next design migration

sales@thalia-da.com