

## AMALIA PLATFORM: CIRCUIT PORTING CORE

### Effortless and Error free circuit porting

**Circuit Porting Core (CP-Core)** takes the device mapping file created by *Technology Analyzer* together with the source schematics and generates schematics and testbenches for the target technology. It provides a commercially supported real alternative to in-house script developed solutions which are often error prone.

With Circuit Porting Core, the migration of schematics and test benches are addressed in hours instead of days or weeks.

## Cost effective, time-saving circuit porting

### Reducing design cycle

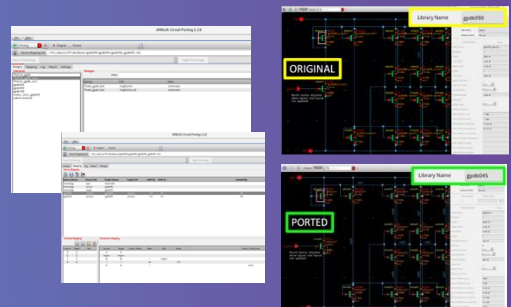
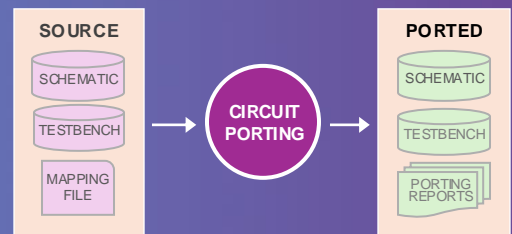
Significantly reduces design cycle times by leveraging electrically comparable devices during the porting of analog, mixed-signal and RF designs to new process nodes

### ML engine

The custom-made ML engine is trained across several foundries and technology nodes. The engine can be easily trained on new technologies

### Designer productivity

Automated hierarchical generation of schematic and testbenches in hours instead of days/weeks



## Key Features

### Automated, hierarchical schematic and testbench migration

Maintaining integrity of instances and net names

### Automatic instance splitting

Conforming the design to meet process technology requirements

### Parameter mapping, scaling and modification

Enhanced with conditional rules

### User friendly error reporting & fixing

Highlighting mismatches in cell parameters between source & ported designs at circuit level

### Outputs

Ported schematic, testbench, porting report and log file

### Integrated with leading EDA solutions

Cadence® Virtuoso®

Other integrations to follow

### Trademark acknowledgment

Cadence and Virtuoso are Marks of Cadence Design Systems

Use of these names and other company's and products does not imply endorsement.



## The AMALIA difference

AMALIA Platform is an advanced, comprehensive IP reuse solution.

Leveraging automation and AI/ML capabilities, it drastically reduces the complexity, cost and time traditionally associated with migrating and enhancing existing analog, mixed-signal and RF design IPs.

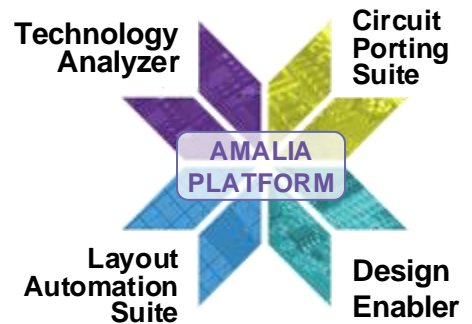
Four functional solutions provide a powerful portfolio to successfully accelerate analog reuse.

## Navigating the design migration challenge

Migrating an IC or IP to a different process technology or node unlocks new revenue opportunities, enhancing performance and secure a reliable second source supply.

However, design migration is a conundrum for semiconductor business leaders. While it opens new business opportunities, analog and mixed-signal migrations are expensive and fraught with risks.

AMALIA Platform addresses this challenge by providing a proven, unique end-to-end solution which reduces the engineering effort from man-months to man-weeks, reducing costs while significantly mitigating risks.



## Contact us today

Contact our sales team today to discuss how AMALIA can accelerate your next design migration

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