![Thalia logo]

Accelerating analog design reuse

AMALIA PLATFORM: CIRCUIT PORTING PROFESSIONAL

Automated verification for migrated schematics

Circuit Porting Professional (CP-Pro) automates the validation of migrated schematics and test benches. It guarantees integrity of the ported database and with comparison of simulations results between source and target.

Instead of spending weeks manually simulating and collating data across all PVT corners, CP-Pro automates and manages the process, delivering organized results.

With pre-analyzed results, designers can focus on making the required design adjustments and improvements.

Automatic verification during circuit porting

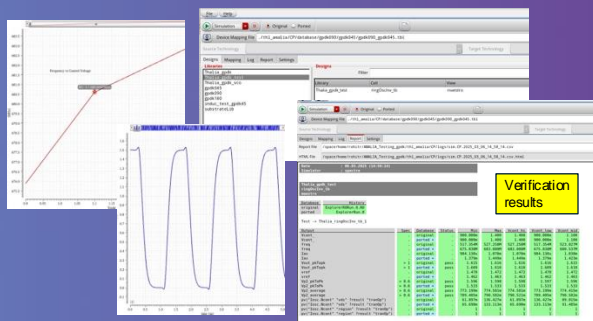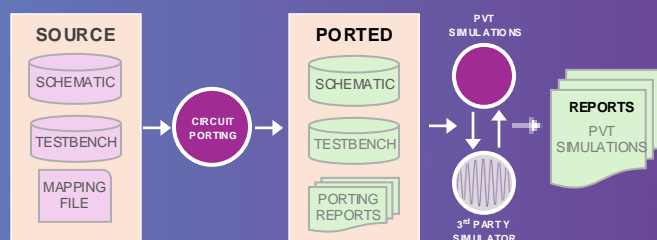## PVT corner simulations & inituitve comparison reports
Automatic execution of all PVT corner simulations of the source and ported design. Provides insight through visually intuitive comparison reports

Fully automated
CP-Pro can execute simulations in parallel dramatically shortening end-to-end simulation time (limited by compute and simulator license availability)

Time and cost efficiency
Reduces design cycle time and relieves engineers from repetitive tasks freeing them to focus on the critical tasks

Key Features

Parallelize verifications
Multiple simulations and Maestro states can be run concurrently limited only by compute and simulation resources

Comparison reports of PVT simulation
Color highlighting identifies performance metrics which have not been met compared to the source

User friendly error reporting & fixes
Highlighting mismatches in cell parameters between source & ported designs at circuit level

Outputs
Ported schematic, testbench, simulation comparison report, porting report and log file

Integrated with leading EDA solutions
Cadence® Virtuoso® ADE
Siemens EDA Analog FastSPICE

Thalia
Accelerating analog IP reuse

The AMALIA difference

AMALIA Platform is an advanced, comprehensive IP reuse solution.

Leveraging automation and AI/ML capabilities, it drastically reduces the complexity, cost and time traditionally associated with migrating and enhancing existing analog, mixed-signal and RF design IPs.
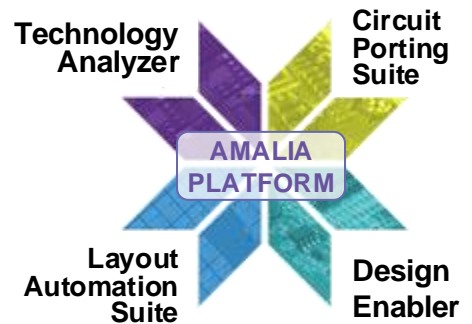
Four functional solutions provide a powerful portfolio to successfully accelerate analog reuse.

Navigating the design migration challenge

Migrating an IC or IP to a different process technology or node unlocks new revenue opportunities, enhancing performance and secure a reliable second source supply.

Technology Analyzer

Circuit Porting Suite

AMALIA PLATFORM

Layout Automation Suite

Design Enabler

However, design migration is a conundrum for semiconductor business leaders. While it opens new business opportunities, analog and mixed-signal migrations are expensive and fraught with risks.

AMALIA Platform addresses this challenge by providing a proven, unique end-to-end solution which reduces the engineering effort from man-months to man-weeks, reducing costs while significantly mitigating risks.

Contact us today

Contact our sales team today to discuss how AMALIA can accelerate you next design migration

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