

AMALIA PLATFORM: LAYOUT AUTOMATION SUITE

AMALIA Platform Layout Automation Suite (LA) preserves the intrinsic value demonstrated by production silicon, delivering the final step in the design migration flow.

It utilizes a custom AI Design Transformer engine to execute analog physical implementation, leveraging the source layout and PDK. Critically, the migrated layout retains the existing floorplan, ensuring DRC/LVS integrity and minimizing design iterations.

Maintaining source floorplan, ensuring DRC integrity

Maintains floorplan & Placement of devices

Preserves the *Silicon Intelligence* of existing silicon and maintains the same floorplan and placement

Intelligent compliance assurance

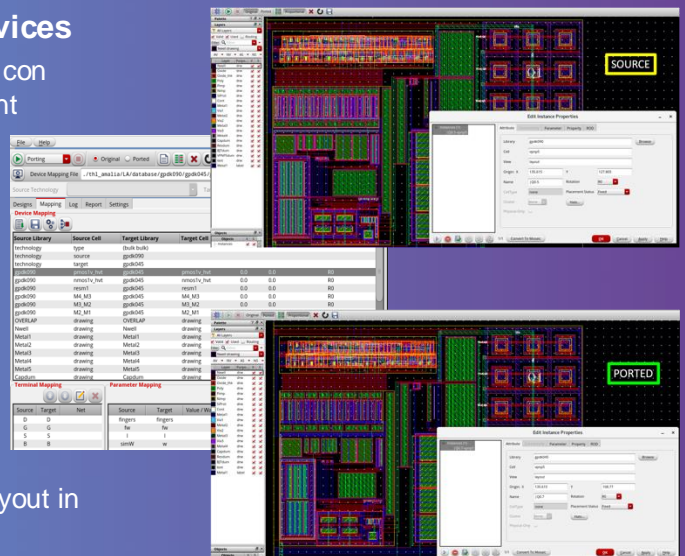
Built-in DRC ensuring layout adherence to constraints

AI driven features

Automated device stretching, metal stacking and electromigration features

Layout generation in days

AI and Automation resulting in generation of Layout in days versus weeks/months



Key Features

Placement and floorplan maintenance

Maintains the integrity of placement and floor plans and preserves *Silicon Intelligence*.

Device and property mapping & updating

Accurately maps devices, properties, vias and layers. Update the layout with new schematic device values

AI driven device stretching

Reshape the devices to customize the layout

Design rule checks and electromigration

Incorporates DR checks and addresses EM requirements

Outputs

OA layout including p-cells, LVS reports, metal stack variants

Integration with leading EDA solutions

Cadence Virtuoso®

Other integrations to follow

Trademark acknowledgment

Cadence and Virtuoso are Marks of Cadence Design Systems

Use of these names and other company's and products does not imply endorsement.



The AMALIA difference

AMALIA Platform is an advanced, comprehensive IP reuse solution.

Leveraging automation and AI/ML capabilities, it drastically reduces the complexity, cost and time traditionally associated with migrating and enhancing existing analog, mixed-signal and RF design IPs.

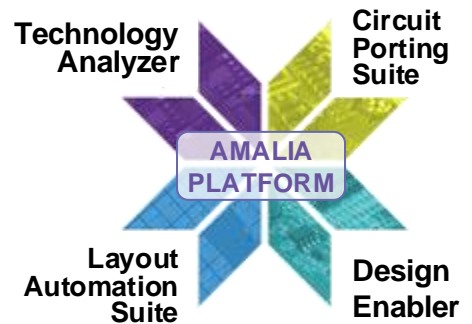
Four functional solutions provide a powerful portfolio to successfully accelerate analog reuse.

Navigating the design migration challenge

Migrating an IC or IP to a different process technology or node unlocks new revenue opportunities, enhancing performance and secure a reliable second source supply.

However, design migration is a conundrum for semiconductor business leaders. While it opens new business opportunities, analog and mixed-signal migrations are expensive and fraught with risks.

AMALIA Platform addresses this challenge by providing a proven, unique end-to-end solution which reduces the engineering effort from man-months to man-weeks, reducing costs while significantly mitigating risks.



Contact us today

Contact our sales team today to discuss how AMALIA can accelerate your next design migration

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