

AMALIA PLATFORM: TECHNOLOGY ANALYZER

Unlock the future of IC design migration

Technology Analyzer (TA) delivers a fully automated electrical analysis of the source and target devices using an evolving machine learning-driven engine. The analysis efficiently creates the complete mapping between source and target design devices. Moreover, the solution is driven by user-controlled preference rules and provides easy-to-read visual comparison of 'one-to-one' or 'one-to-many' technologies.

Technology Analyzer simplifies decision-making

Fast technology assessment

Early analysis of source and target technologies helps select the best process node to deliver the economic competitive solution

ML driven self-training of TA

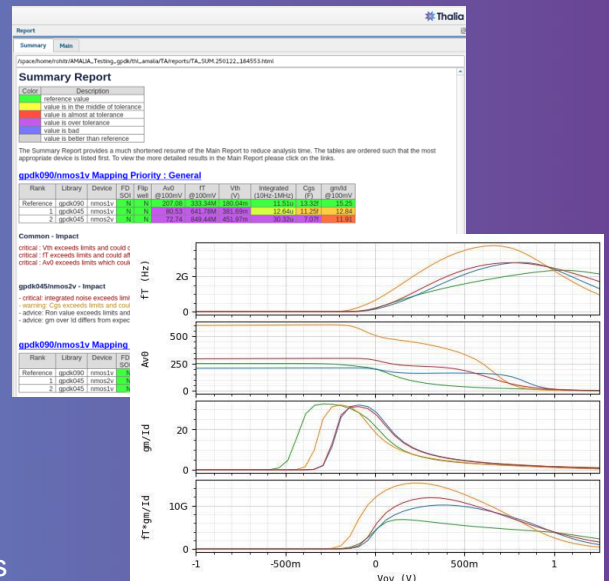
ML engine eases the training of TA to new technologies and node. Replaces time consuming manual effort from man-months to man-days

Constraint driven device mapping

Systematically identifies target devices most closely matching the electrical characteristics compared to the source

Easily compare device options

Visual compare auto-generated simulation waveforms for source and target devices.



Key Features

Preference-driven device mapping

Automatic mapping to target technology, including user selected criteria, e.g. to optimize leakage or noise

Device characterization extraction

Including key parameters such as (threshold voltage (V_{th}), transition frequency (F_t) and transconductance over drain current (gm/Id))

ML Engine allows for seamless training

Trained in several nodes & technologies

Outputs

Mapping file, Technology analysis report

Integrates with leading EDA solutions

Cadence® Spectre®
Siemens EDA Analog FastSPICE Platform

Trademark acknowledgment

Cadence and Virtuoso are Marks of Cadence Design Systems
Use of these names and other company's and products does not imply endorsement.



The AMALIA difference

AMALIA Platform is an advanced, comprehensive IP reuse solution.

Leveraging automation and AI/ML capabilities, it drastically reduces the complexity, cost and time traditionally associated with migrating and enhancing existing analog, mixed-signal and RF design IPs.

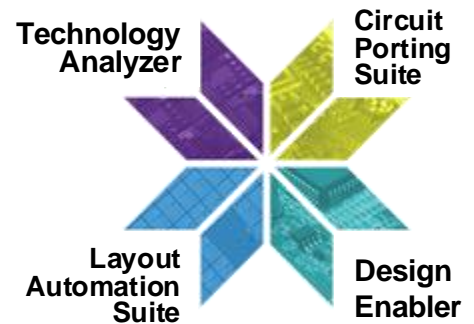
Four functional solutions provide a powerful portfolio to successfully accelerate analog reuse.

Navigating the design migration challenge

Migrating an IC or IP to a different process technology or node unlocks new revenue opportunities, enhancing performance and secure a reliable second source supply.

However, design migration is a conundrum for semiconductor business leaders. While it opens new business opportunities, analog and mixed-signal migrations are expensive and fraught with risks.

AMALIA Platform addresses this challenge by providing a proven, unique end-to-end solution which reduces the engineering effort from man-months to man-weeks, reducing costs while significantly mitigating risks.



Contact us today

Contact our sales team today to discuss how AMALIA can accelerate your next design migration

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